

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: DELORME et al.

Title: BIPOLAR DIFFERENTIAL INPUT STAGE WITH INPUT BIAS CURRENT  
CANCELLATION CIRCUIT

Commissioner for Patents  
P.O. 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

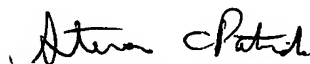
In accordance with PTO procedure regarding all patent applications filed after June 30, 2003, we have not submitted the U.S. patent references cited on the enclosed PTO form 1449.

The filing of this information disclosure statement shall not be construed as a representation that a search has been made (37 C.F.R. 1.97(g)), an admission that the information cited is, or is considered to be, material to patentability, or that no other material information exists.

The filing of this information disclosure statement shall not be construed as an admission against interest in any manner. Notice of January 9, 1992, 1135 O.G. 13-25, at 25.

Respectfully submitted,

February 27, 2004

  
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M/S2-A8SJ2351USIDS-1.97(g).doc

FORM PTO-1449 (Modified)  INFORMATION DISCLOSURE CITATION IN AN APPLICATION  (Use several sheets if necessary)	Docket No. A8SJ2351US	Application Number
	Applicant DELORME et al.	
	Filing Date	Group Art Unit

## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number							Date	Name	Class	Subclass	Filing Date If Appropriate
	4	5	7	5	6	8	5	3/11/86	DOBKIN et al.	330	261	

## FOREIGN PATENT DOCUMENTS

	Document Number							Date	Country	Class	Subclass	Translation	
												Yes	No

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	1	LINEAR TECHNOLOGY, "DUAL/QUAD 135 $\mu$ A, 14Nv/Hz, RAIL-TO-RAIL OUTPUT PRECISION Op Amp, LT6011/LT6012, p. 1-16.
Examiner		Date Considered
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through		